

### REMARKS

Claim 1 was rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent to Smith et al. (US 6188281). Figures 6, 7A-C, 8A-B, 10 and 15-16 were cited as purportedly disclosing the claimed invention. While Smith et al. do show a transconductance circuit 300 which further includes an amplifier 310 and a concave compensation circuit 320 coupled in parallel (see col. 4, 1152-58) in Figure 6, for example, Smith et al. do not show what is required by the claim as presently amended, namely:

*first DC coupled transconductance stage comprising MOSFET amplification devices operably coupled to produce a first differential current from a differential input voltage based on a first bias voltage;*

*second DC coupled transconductance stage comprising MOSFET amplification devices operably coupled to produce a second differential current based on the differential input voltage and a second bias voltage, wherein an output current of the class AB voltage current converter is a sum of the first differential current and the second[[ary]] differential current; and*

*biasing circuit operably coupled to produce the first bias voltage and the secondary bias voltage, wherein the first bias voltage is greater than the secondary bias voltage.*

Smith et al. do not teach an amplifier design having MOSFET amplification devices operably disposed to provide first and second transconductance stages whose output currents are summed to define the output current of the amplifier. Smith does not teach that the concave compensation circuit of Smith is configured to produce an output current that is a sum of the first and second differential currents. Further, Smith et al. do not show a biasing circuit operably disposed to *produce the first bias voltage and the secondary bias voltage, wherein the first bias voltage is greater than the secondary bias voltage.*

Claims 1-4, 7-9 and 12-14 were rejected under 35 U. S.C. 102(e) as being anticipated by Behzad et al. (US 6,496,067).

According to the USPTO Patent Full-Text and Image Database, U.S. Patent 6,496,067 which is being relied upon for the rejections under 35 U.S.C. 102(e), the inventor and assignee information is as follows:

Inventors: **Behzad; Arya Reza** (Poway, CA); **Lin; Li** (Mountain View, CA)  
Assignee: **Broadcom** (Irvine, CA)  
Filed: **January 7, 2002**

As the United States Code provides under 35 U.S.C. 102(e) that the rejection requires the invention to be described in a patent granted on an application for patent by another, the Applicant believes that the rejection of the claims under 35 U.S.C. 102(e) is improper and should be removed because the inventor and assignee of the present case and the cited case are one and the same thus not satisfying the requirement that the reference be "by another". 35 U.S.C. 102(e) states:

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent <emphasis added>.

As such, the Applicant urges that rejections in numbered section 4. of the Official Action and based upon 35 U.S.C. 102(e) are overcome.

Claims 13 and 14 were the same. Claim 14 is cancelled.

Claims 5-6, 10-11 and 15-16 were objected to as being dependent upon rejected base claims but were indicated to be allowable. The Applicant appreciates such indication.

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Applicant believes the case is now in condition for allowance. Allowance of all claims is respectfully requested. The Commissioner is authorized to charge any additional fees and/or credit any overpayment to Deposit Account No. 50-2126 (BP2475).

Respectfully submitted,

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